PENDING CLAIMS AS AMENDED

Please amend the claims as follows:

1. (Currently Amended): A wireless communication system operative for transmission of packet data and low delay data on a plurality of forward link transmission channels, the system comprising:

a first set of forward link channels within the plurality of transmission channels, the first set of channels being assigned to packet data transmissions and packet data being transmitted in frames;

a second set of forward link channels within the plurality of transmission channels, the second set of channels being assigned to low delay data transmissions; and

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a forward link signaling channel within the plurality of transmission channels, the signaling channel being assigned to message transmissions, wherein each message corresponds to packet data and identifies a packet data target recipient.

2. (Original): The wireless communication system of claim 1, wherein a first message is transmitted on the signaling channel concurrently with an associated first packet data frame, and wherein the first message identifies a first packet data recipient associated with the first packet data frame.

3. (Original): The wireless communication system of claim 1, wherein the first message identifies a subset of the first set of channels assigned to transmission of the first packet data.

4. (Original): The wireless communication system of claim 1, wherein the first message identifies a coding scheme used for transmission of the first packet data.

5. (Currently Amended): A wireless apparatus operative within the system of claim 1, the wireless apparatus operative to receive packet data via at least one of the first set of channels and to receive messages via the signaling channel, the wireless apparatus comprising:

a buffer operative to buffer [store] packet data received via at least one of the first set of channels;

a processor coupled to the buffer, the processor operative to determine target recipient information from the received messages; and

a decoder coupled to the processor, the decoder operative to decode data packets received if the wireless apparatus is a target recipient and ignore data packets if the wireless apparatus is not the target recipient.

6. (Original): The wireless apparatus of claim 5, wherein the target recipient information may identify multiple recipients.

7. (Original): The wireless apparatus of claim 6, further comprising:

a memory storage device coupled to the processor, the memory storage device storing computer readable instructions operative to control the decoder.

8. (Original): In a wireless communication system, the system supporting packet data transmissions and low delay data transmissions over a plurality of transmission channels, a method comprising:

transmitting packet data via a set of packet data channels; and

transmitting control information associated with the packet data via a signaling channel, wherein the signaling channel is separate from the set of packet data channels, and wherein the control information identifies a target recipient of associated packet data.

9. (Original): The method of claim 8, wherein the control information further identifies a coding scheme for the packet data.

10. (Original): The method of claim 9, further comprising:

receiving data requests from a plurality of mobile units; and

determining a transmission schedule according to the data requests.

11. (Original): The method of claim 10,

assigning a priority level to each of the plurality of mobile units; and

determining a traffic schedule among the plurality of mobile units based on priority level.

12. (Original): The method of claim 11, wherein a high priority is given to a mobile unit experiencing less interference than other of the plurality of mobile units.

13. (Currently Amended): A wireless apparatus operative to receive packet data via at least one of [the] a first set of channels, the wireless apparatus comprising:

a processor operative to receive messages via a signaling channel and to determine target recipient information and coding information from received messages; and

a data rate determination unit operative to calculate a data rate in accordance with the target recipient information and the coding information.

14. (Original): The apparatus of claim 13, wherein the apparatus is operative within a wireless communication system supporting high rate packet data transmissions and low delay data transmissions.

15. (Original): The apparatus of claim 13, further comprising:

a buffer coupled to the processor, the buffer operative to store packet data received via the at least one of the first set of channels;

a decoder coupled to the processor, the decoder operative to decode data packets received if the wireless apparatus is a target recipient and ignore data packets if the wireless apparatus is not the target recipient.

16. (Original): The apparatus of claim 13, wherein the target recipient information identifies multiple target recipients.

17. (Original): The apparatus of claim 13, wherein the coding information is predetermined by a transmitter and is used to encode the packet data, and

wherein the apparatus further comprises:

a decoder coupled to the processor, the decoder responsive to the coding information to decode received packet data.

18. (Currently Amended): A wireless communication system operative for transmission of packet data and low delay data on a plurality of transmission channels, the system comprising:

a first set of forward link channels within the plurality of transmission channels, the first set of channels being assigned to packet data transmissions and packet data being transmitted in frames;

a second set of forward link channels within the plurality of transmission channels, the second set of channels being assigned to low delay data transmissions; and

a forward link signaling channel within the plurality of transmission channels; the signaling channel being assigned to message transmissions, wherein a message corresponds to a packet transmitted on one of the first set of channels, wherein the message identifies a parameter of the packet.

19. (Cancelled)

20. (Original): The wireless communication system of claim 18, wherein the message is sent on the forward link from the base station to the mobile station.

21. (Previously Presented): A wireless apparatus operative to process packet data via at least one of a first set of channels and to process low delay data transmissions via at least one of a second set of channels, the wireless apparatus comprising:

means for processing data in frames on at least one of the first set of channels;

means for processing low delay data on at least one of the second set of channels;

means for encoding a message corresponding to a particular packet and identifying a parameter of the packet; and

means for sending the message on a signaling channel.

22. (Previously Presented): A wireless apparatus operative to send or receive packet data via at least one of a first set of channels and to send or receive low delay data transmissions via at least one of a second set of channels, the wireless apparatus comprising:

means for processing packet data in frames on at least one of the first set of channels;

means for processing low delay data on at least one of the second set of channels;

means for receiving a message corresponding to a particular packet on a signaling channel;

means for decoding the message corresponding to the particular packet and identifying a parameter of the packet; and

means for using the parameter in the reception of the particular packet.

23. (Previously Presented): The wireless communication system of claim 18, wherein the parameter is a sequence number for the packet.

24. (Previously Presented): The wireless communication system of claim 18, wherein the parameter comprises coding and modulation used in transmitting the packet.

25. (Previously Presented): The wireless communication system of claim 24, wherein the parameter is a first identifier, wherein the first identifier is stored in a memory storage device corresponding to the coding and modulation.

26. (Previously Presented): A wireless apparatus operative to receive packet data via at least one of the first set of channels, the wireless apparatus comprising:

a processor operative to receive messages via a signaling channel and to determine packet parameter information and coding information from received messages; and

a packet decoder operative to decode the received messages in accordance with the packet parameter information and the coding information.

27. (Previously Presented): A wireless communication apparatus supporting packet data communications and low delay data communications over a plurality of transmission channels, the apparatus comprising:

a memory storage device adapted for storing computer-readable instructions; and

a processor adapted for processing said computer-readable instructions to:

receive packet data via a set of packet data channels; and

receive control information associated with the packet data via a signaling channel, wherein the signaling channel is separate from the set of packet data channels, and wherein the control information identifies a target recipient of associated packet data.

28. (New): A wireless receiving system operative for receiving packet data and low delay data on a plurality of transmission channels, the system comprising:

a receiver component for receiving packet data transmissions and packet data being transmitted in frames;

a receiver component for receiving low delay data transmissions; and

a receiver component for receiving message transmissions, wherein each message identifies a packet data target recipient.

29. (New): A method, comprising:

receiving a first message on a signaling channel, the first message identifying a first packet of data and a target recipient for the first packet of data; and

receiving the first packet of data on a low delay data channel concurrently with receiving the first message on the signaling channel.

30. (New): The method of claim 28, further comprising:

decoding the first packet of data.

31. (New): An apparatus, comprising:

means for receiving a message on a signaling channel, the message identifying a first packet of data and a target recipient for the first packet of data; and

means for receiving the first packet of data on a low delay data channel concurrently with receiving the message on the signaling channel.

32. (New): The apparatus of claim 37, further comprising:

means for decoding the message.

33. (New): The apparatus of claim 38, wherein the message identifies the apparatus as the target recipient.

34. (New): The method of claim 28, wherein the low delay data channel is one of a first set of channels, and wherein the first message identifies a subset of the first set of channels.

35. (New): The method of claim 28, wherein the first message identifies a coding scheme used for transmission of the first packet data.

36. (New): In a wireless receiving system, the system supporting packet data and low delay data transmission over a plurality of transmission channels, a method comprising:

receiving packet data via a set of packet data channels; and

receiving control information associated with the packet data via a signaling channel, wherein the signaling channel is separate from the set of packet data channels, and wherein the control information identifies a target recipient of associated packet data.

37. (New): A wireless receiving system operative for receipt of packet data and low delay data on a plurality of transmission channels, the system comprising:

a receiver component for receiving packet data transmissions and packet data being transmitted in frames;

a receiver component for receiving low delay data transmissions; and

a receiver component for receiving message transmissions, wherein a message corresponds to a packet transmitted on one of the first set of channels, wherein the message identifies a parameter of the packet.

38. (New): A computer data signal embodied on a carrier wave, characterized by:

a plurality of packet data frames transmitted on a first set of forward link transmission channels;

a plurality of low delay data transmitted on a second set of forward link transmission channels; and

a plurality of messages transmitted on a forward link signaling channel.

39. (New): The computer data signal of claim 34, wherein each message comprises:

a packet data parameter for reception of packet data.

40. (New): The computer data signal of claim 34, wherein each message comprises:

a target recipient identifier.